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Artix-7 FPGA Power Benefits Achieving Optimal Performance-per-Watt with Kintex-7 FPGAs ~~Inside the Virtex-7 FPGA VC709 Connectivity Kit~~ Inside the Artix-7 FPGA AC701 Evaluation Kit Embedded Systems Design with Platform FPGAs part 2 ~~Kintex-7 Fpga Embedded Targeted~~

The Kintex®-7 Embedded Kit includes the components of the Kintex-7 KC705 Base Evaluation Kit plus all additional soft content that embedded designers need to quickly design their high-performance embedded systems. This includes the embedded targeted reference design and relevant tools such as Vivado® Design Suite. Key Features & Benefits

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~~Xilinx Kintex-7 FPGA Embedded Kit - Adaptable. Intelligent.~~

The targeted reference designs (TRDs) are built around a MicroBlaze soft processor with various peripherals to enable embedded applications. Two designs are provided as part of the TRD: the KC705_System (BIST system) and the video demonstration system.

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The Kintex-7 FPGA KC705 Evaluation Kit includes all the basic components of hardware, design tools, IP, and pre-verified reference designs including a targeted design enabling high-performance serial connectivity and advanced memory interfacing.

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Kintex-7 FPGA Embedded Targeted Reference Design User Guide Vivado Design Suite 20132 UG985 (v10), August 29, 2013 Kintex-7 FPGA KC705 Embedded Kit - Xilinx The Kintex®-7 FPGA embedded kit conveniently delivers the key components of the Xilinx® Embedded Targeted Design Platform (TDP) required for developing embedded software and hardware in a wide range of applications in the broadcast ...

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Kintex-7 FPGA Embedded Kit TRD v2.0 for ISE Design Suite 14.1 with CES Silicon In order to set the environment variables, namely XILINX and XILINX_PLANAHEAD, it is required that settings32.sh

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or settings64.sh be sourced from the ISE installation folder.

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pre-verified reference designs including a targeted design enabling high performance serial connectivity and advanced memory interfacing.

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The Kintex®-7 FPGA KC705 Evaluation Kit includes all the basic components of hardware, design tools, IP, and pre-verified reference designs including a targeted design enabling high-performance serial connectivity and advanced memory interfacing. The included pre-verified reference designs and industry-standard FPGA Mezzanine Connectors (FMC) allow scaling and customization with daughter cards.

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additional soft content that embedded designers need to quickly design their high-performance embedded systems.

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range of applications in the broadcast, industrial, medical, and aerospace and defense markets.

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The Kintex-7 FPGA Embedded Kit is priced at \$1895

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and is open for order entry. For more information or to download the Kintex-7 FPGA Embedded Targeted Reference Design, please visit www.xilinx.com/k7embkit. Customers can also contact their local Xilinx sales representative.

~~Xilinx Kintex 7 FPGA Embedded Kit Accelerates Productivity ...~~

The Kintex®-7 FPGA Connectivity Kit is a 20Gb/s platform for high-bandwidth and high-performance applications containing all the necessary hardware, software and IP to power quickly through your evaluation and development of connectivity systems. This includes a 20Gb/s targeted reference design

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featuring PCI Express, a DMA IP core from Northwest Logic, 10GBase-R, AXI, and a Virtual FIFO ...

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